

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17	(static statically) same (reassembly near3 (buffer queue block))	US-PGPUB; USPAT; EPO	OR	ON	2008/01/11 10:08
L2	214	((sequence adj1 number)destination egress) same (reassembly near3 (buffer queue block))	US-PGPUB; USPAT; EPO	OR	ON	2008/01/11 10:09
L3	57	(list pointer array record)same ((sequence adj1 number)destination egress) same (reassembly near3 (buffer queue block))	US-PGPUB; USPAT; EPO	OR	ON	2008/01/11 10:09
L4	1	10/752921	US-PGPUB; USPAT; EPO	OR	ON	2008/01/11 12:04
L5	1	L4	US-PGPUB; USPAT	OR	ON	2008/01/11 12:04
L6	1	5 and (selective\$2)	US-PGPUB; USPAT	OR	ON	2008/01/11 12:05
L7	1	5 and (static\$2)	US-PGPUB; USPAT	OR	ON	2008/01/11 12:06
L8	5	(reassembly adj1 (buffer block queue)) same (static\$4 same dynamic\$4)	US-PGPUB; USPAT	OR	ON	2008/01/11 12:08
L9	119	((buffer block queue)) with (static\$4 with dynamic\$4) with mode	US-PGPUB; USPAT	OR	ON	2008/01/11 13:27
L10	3	((buffer block queue)) with (static\$4 with dynamic\$4) with mode with (allocat\$3)	US-PGPUB; USPAT	OR	ON	2008/01/11 13:48
L11	1	"20040047361".pn.	US-PGPUB; USPAT	OR	ON	2008/01/11 13:48
L12	1	11 and (computer program)	US-PGPUB; USPAT	OR	ON	2008/01/11 14:56
L13	6366	(370/230 370/235 370/412).ccls.	US-PGPUB; USPAT	OR	ON	2008/01/11 14:56

Day : Friday  
Date: 1/11/2008

Time: 15:02:46

# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = BIRAN

First Name = GIORA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09406527</a>	<a href="#">6345347</a>	150	09/27/1999	ADDRESS PROTECTION USING A HARDWARE-DEFINED APPLICATION KEY	BIRAN, GIORA
<a href="#">09591216</a>	<a href="#">6829647</a>	150	06/09/2000	SCALEABLE HARDWARE ARBITER	BIRAN, GIORA
<a href="#">09772376</a>	<a href="#">6658521</a>	150	01/30/2001	METHOD AND APPARATUS FOR ADDRESS TRANSLATION ON PCI BUS OVER INFINIBAND NETWORK	BIRAN, GIORA
<a href="#">09773053</a>	Not Issued	161	01/31/2001	Data communications interfaces	BIRAN, GIORA
<a href="#">09810940</a>	<a href="#">7050437</a>	150	03/16/2001	WIRE SPEED REASSEMBLY OF DATA FRAMES	BIRAN, GIORA
<a href="#">09810941</a>	<a href="#">6947430</a>	150	03/16/2001	NETWORK ADAPTER WITH EMBEDDED DEEP PACKET PROCESSING	BIRAN, GIORA
<a href="#">10470366</a>	Not Issued	40	12/22/2003	Providing control information to a management processor of a communications switch	BIRAN, GIORA
<a href="#">10619960</a>	Not Issued	71	07/15/2003	Controlling flow of data between data processing systems via a memory	BIRAN, GIORA
<a href="#">10619988</a>	<a href="#">7320041</a>	150	07/15/2003	CONTROLLING FLOW OF DATA BETWEEN DATA PROCESSING SYSTEMS VIA A MEMORY	BIRAN, GIORA
<a href="#">10619989</a>	Not Issued	41	07/15/2003	TRANSFERRING INTERRUPTS FROM A PERIPHERAL DEVICE TO A HOST COMPUTER SYSTEM	BIRAN, GIORA
<a href="#">10724783</a>	Not Issued	30	12/01/2003	Asynchronous completion notification for an RDMA system	BIRAN, GIORA
<a href="#">10724960</a>	Not	120	12/01/2003	Parallel TCP sender	BIRAN, GIORA

	Issued			implementation	
<u>10725740</u>	Not Issued	30	12/02/2003	RDMA completion and retransmit system and method	BIRAN, GIORA
<u>10733588</u>	Not Issued	41	12/11/2003	Data transfer error checking	BIRAN, GIORA
<u>10733594</u>	Not Issued	41	12/11/2003	Reducing number of write operations relative to delivery of out-of-order RDMA send messages	BIRAN, GIORA
<u>10733630</u>	<u>7177941</u>	150	12/11/2003	INCREASING TCP RE-TRANSMISSION PROCESS SPEED	BIRAN, GIORA
<u>10733734</u>	Not Issued	71	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	BIRAN, GIORA
<u>10734037</u>	Not Issued	71	12/11/2003	In-order delivery of plurality of RDMA messages	BIRAN, GIORA
<u>10747433</u>	Not Issued	71	12/29/2003	Application and verb resource management	BIRAN, GIORA
<u>10752731</u>	<u>7298749</u>	150	01/07/2004	COMPLETION COALESCING BY TCP RECEIVER	BIRAN, GIORA
<u>10752921</u>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	BIRAN, GIORA
<u>10905810</u>	Not Issued	61	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY INITIATOR	BIRAN, GIORA
<u>10905811</u>	Not Issued	61	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY TARGET	BIRAN, GIORA
<u>10905812</u>	Not Issued	94	01/21/2005	RECOVERY FROM ISCSI CORRUPTION WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10905813</u>	Not Issued	61	01/21/2005	iSCSI DATAMOVER INTERFACE AND FUNCTION SPLIT WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10905814</u>	Not Issued	71	01/21/2005	DIRECT ACCESS OF SCSI BUFFER WITH RDMA ATP MECHANISM BY iSCSI TARGET AND/OR INITIATOR	BIRAN, GIORA
<u>10905815</u>	Not Issued	61	01/21/2005	SCSI BUFFER MEMORY MANAGEMENT WITH RDMA ATP MECHANISM	BIRAN, GIORA
<u>10907507</u>	Not	41	04/04/2005	CONNECTION	BIRAN, GIORA

	Issued			ESTABLISHMENT ON A TCP OFFLOAD ENGINE	
<u>10908886</u>	Not Issued	30	05/31/2005	Method For Transmitting Window Probe Packets	BIRAN, GIORA
<u>11064568</u>	Not Issued	41	02/24/2005	Computer system bus bridge	BIRAN, GIORA
<u>11064745</u>	7234017	150	02/24/2005	COMPUTER SYSTEM ARCHITECTURE FOR A PROCESSOR CONNECTED TO A HIGH SPEED BUS TRANSCEIVER	BIRAN, GIORA
<u>11065818</u>	Not Issued	41	02/25/2005	Method, system, and computer program product for virtual adapter destruction on a physical adapter that supports virtual adapters	BIRAN, GIORA
<u>11065821</u>	Not Issued	30	02/25/2005	Method and system for native virtualization on a partially trusted adapter using adapter bus, device and function number for identification	BIRAN, GIORA
<u>11065829</u>	Not Issued	30	02/25/2005	Data processing system, method, and computer program product for creation and initialization of a virtual adapter on a physical adapter that supports virtual adapter level virtualization	BIRAN, GIORA
<u>11065830</u>	Not Issued	71	02/25/2005	System and method for host initialization for an adapter that supports virtualization	BIRAN, GIORA
<u>11065951</u>	7260664	150	02/25/2005	INTERRUPT MECHANISM ON AN IO ADAPTER THAT SUPPORTS VIRTUALIZATION	BIRAN, GIORA
<u>11066096</u>	Not Issued	41	02/25/2005	System and method for destroying virtual resources in a logically partitioned data processing system	BIRAN, GIORA
<u>11066201</u>	Not Issued	30	02/25/2005	System and method for modification of virtual adapter resources in a logically partitioned data processing system	BIRAN, GIORA
<u>11066353</u>	Not Issued	71	02/25/2005	System, method, and computer program product for a fully trusted adapter validation of incoming memory mapped I/O operations on a physical adapter that supports virtual adapters or virtual resources	BIRAN, GIORA

<a href="#"><u>11066419</u></a>	Not Issued	30	02/25/2005	Association of memory access through protection attributes that are associated to an access control level on a PCI adapter that supports virtualization	BIRAN, GIORA
<a href="#"><u>11066424</u></a>	Not Issued	71	02/25/2005	Method, system and program product for differentiating between virtual hosts on bus transactions and associating allowable memory access for an input/output adapter that supports virtualization	BIRAN, GIORA
<a href="#"><u>11066517</u></a>	Not Issued	30	02/25/2005	System and method for virtual resource initialization on a physical adapter that supports virtual resources	BIRAN, GIORA
<a href="#"><u>11066518</u></a>	Not Issued	30	02/25/2005	System and method of virtual resource modification on a physical adapter that supports virtual resources	BIRAN, GIORA
<a href="#"><u>11066519</u></a>	Not Issued	89	02/25/2005	Native virtualization on a partially trusted adapter using PCI host bus, device, and function number for identification	BIRAN, GIORA
<a href="#"><u>11066521</u></a>	Not Issued	89	02/25/2005	System and method for virtual adapter resource allocation	BIRAN, GIORA
<a href="#"><u>11068664</u></a>	Not Issued	71	02/28/2005	Method and system for fully trusted adapter validation of addresses referenced in a virtual host transfer request	BIRAN, GIORA
<a href="#"><u>11071553</u></a>	Not Issued	30	03/03/2005	Recovery from MSS change	BIRAN, GIORA
<a href="#"><u>11086713</u></a>	Not Issued	71	03/22/2005	System and method for efficient implementation of a shared receive queue	BIRAN, GIORA
<a href="#"><u>11132100</u></a>	Not Issued	30	05/18/2005	Network acceleration architecture	BIRAN, GIORA
<a href="#"><u>11132101</u></a>	Not Issued	30	05/18/2005	Receive flow in a network acceleration architecture	BIRAN, GIORA

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Last Name	First Name
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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = EPSHTEIN

First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09218407</a>	<a href="#">6225924</a>	150	12/22/1998	TRANSMISSION CODING METHOD AND DEVICE	EPSHTEIN, MARK
<a href="#">10752921</a>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	EPSHTEIN, MARK

**Inventor Search Completed: No Records to Display.**

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**PALM INTRANET**

Time: 15:03:13

**Inventor Name Search Result**

Your Search was:

Last Name = MAKHERVAKS

First Name = VADIM

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09772376</a>	<a href="#">6658521</a>	150	01/30/2001	METHOD AND APPARATUS FOR ADDRESS TRANSLATION ON PCI BUS OVER INFINIBAND NETWORK	MAKHERVAKS, VADIM
<a href="#">10724783</a>	Not Issued	30	12/01/2003	Asynchronous completion notification for an RDMA system	MAKHERVAKS, VADIM
<a href="#">10725740</a>	Not Issued	30	12/02/2003	RDMA completion and retransmit system and method	MAKHERVAKS, VADIM
<a href="#">10733588</a>	Not Issued	41	12/11/2003	Data transfer error checking	MAKHERVAKS, VADIM
<a href="#">10733594</a>	Not Issued	41	12/11/2003	Reducing number of write operations relative to delivery of out-of-order RDMA send messages	MAKHERVAKS, VADIM
<a href="#">10733630</a>	<a href="#">7177941</a>	150	12/11/2003	INCREASING TCP RE-TRANSMISSION PROCESS SPEED	MAKHERVAKS, VADIM
<a href="#">10733668</a>	<a href="#">7243284</a>	150	12/11/2003	LIMITING NUMBER OF RETRANSMISSION ATTEMPTS FOR DATA TRANSFER VIA NETWORK INTERFACE CONTROLLER	MAKHERVAKS, VADIM
<a href="#">10733734</a>	Not Issued	71	12/11/2003	RDMA network interface controller with cut-through implementation for aligned DDP segments	MAKHERVAKS, VADIM
<a href="#">10734037</a>	Not Issued	71	12/11/2003	In-order delivery of plurality of RDMA messages	MAKHERVAKS, VADIM
<a href="#">10747433</a>	Not Issued	71	12/29/2003	Application and verb resource management	MAKHERVAKS, VADIM
<a href="#">10752731</a>	<a href="#">7298749</a>	150	01/07/2004	COMPLETION COALESCING	MAKHERVAKS,



				BY TCP RECEIVER	VADIM
<u>10752921</u>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	MAKHERVAKS, VADIM
<u>10904580</u>	Not Issued	120	11/17/2004	Allocation and Scaling of Descriptor Lists.	MAKHERVAKS, VADIM
<u>10905810</u>	Not Issued	61	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY INITIATOR	MAKHERVAKS, VADIM
<u>10905811</u>	Not Issued	61	01/21/2005	RNIC-BASED OFFLOAD OF iSCSI DATA MOVEMENT FUNCTION BY TARGET	MAKHERVAKS, VADIM
<u>10905812</u>	Not Issued	94	01/21/2005	RECOVERY FROM ISCSI CORRUPTION WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10905813</u>	Not Issued	61	01/21/2005	iSCSI DATAMOVER INTERFACE AND FUNCTION SPLIT WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10905814</u>	Not Issued	71	01/21/2005	DIRECT ACCESS OF SCSI BUFFER WITH RDMA ATP MECHANISM BY iSCSI TARGET AND/OR INITIATOR	MAKHERVAKS, VADIM
<u>10905815</u>	Not Issued	61	01/21/2005	SCSI BUFFER MEMORY MANAGEMENT WITH RDMA ATP MECHANISM	MAKHERVAKS, VADIM
<u>10907506</u>	Not Issued	41	04/04/2005	TCP IMPLEMENTATION WITH MESSAGE-COUNT INTERFACE	MAKHERVAKS, VADIM
<u>10907590</u>	Not Issued	71	04/07/2005	LOCKER MANAGER FOR MANAGING ACCESS TO SHARED RESOURCES	MAKHERVAKS, VADIM
<u>10908886</u>	Not Issued	30	05/31/2005	Method For Transmitting Window Probe Packets	MAKHERVAKS, VADIM
<u>11065818</u>	Not Issued	41	02/25/2005	Method, system, and computer program product for virtual adapter destruction on a physical adapter that supports virtual adapters	MAKHERVAKS, VADIM
<u>11065821</u>	Not Issued	30	02/25/2005	Method and system for native virtualization on a partially trusted adapter using adapter bus, device and function number for identification	MAKHERVAKS, VADIM
<u>11065829</u>	Not	30	02/25/2005	Data processing system, method,	MAKHERVAKS,

	Issued			and computer program product for creation and initialization of a virtual adapter on a physical adapter that supports virtual adapter level virtualization	VADIM
<u>11065830</u>	Not Issued	71	02/25/2005	System and method for host initialization for an adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11065951</u>	<u>7260664</u>	150	02/25/2005	INTERRUPT MECHANISM ON AN IO ADAPTER THAT SUPPORTS VIRTUALIZATION	MAKHERVAKS, VADIM
<u>11066096</u>	Not Issued	41	02/25/2005	System and method for destroying virtual resources in a logically partitioned data processing system	MAKHERVAKS, VADIM
<u>11066201</u>	Not Issued	30	02/25/2005	System and method for modification of virtual adapter resources in a logically partitioned data processing system	MAKHERVAKS, VADIM
<u>11066353</u>	Not Issued	71	02/25/2005	System, method, and computer program product for a fully trusted adapter validation of incoming memory mapped I/O operations on a physical adapter that supports virtual adapters or virtual resources	MAKHERVAKS, VADIM
<u>11066419</u>	Not Issued	30	02/25/2005	Association of memory access through protection attributes that are associated to an access control level on a PCI adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11066424</u>	Not Issued	71	02/25/2005	Method, system and program product for differentiating between virtual hosts on bus transactions and associating allowable memory access for an input/output adapter that supports virtualization	MAKHERVAKS, VADIM
<u>11066517</u>	Not Issued	30	02/25/2005	System and method for virtual resource initialization on a physical adapter that supports virtual resources	MAKHERVAKS, VADIM
<u>11066518</u>	Not Issued	30	02/25/2005	System and method of virtual resource modification on a physical adapter that supports	MAKHERVAKS, VADIM

				virtual resources	
<a href="#"><u>11066519</u></a>	Not Issued	89	02/25/2005	Native virtualization on a partially trusted adapter using PCI host bus, device, and function number for identification	MAKHERVAKS, VADIM
<a href="#"><u>11066521</u></a>	Not Issued	89	02/25/2005	System and method for virtual adapter resource allocation	MAKHERVAKS, VADIM
<a href="#"><u>11068664</u></a>	Not Issued	71	02/28/2005	Method and system for fully trusted adapter validation of addresses referenced in a virtual host transfer request	MAKHERVAKS, VADIM
<a href="#"><u>11071553</u></a>	Not Issued	30	03/03/2005	Recovery from MSS change	MAKHERVAKS, VADIM
<a href="#"><u>11086713</u></a>	Not Issued	71	03/22/2005	System and method for efficient implementation of a shared receive queue	MAKHERVAKS, VADIM
<a href="#"><u>11132100</u></a>	Not Issued	30	05/18/2005	Network acceleration architecture	MAKHERVAKS, VADIM
<a href="#"><u>11132101</u></a>	Not Issued	30	05/18/2005	Receive flow in a network acceleration architecture	MAKHERVAKS, VADIM
<a href="#"><u>11132853</u></a>	Not Issued	30	05/19/2005	Transmit flow for network acceleration architecture	MAKHERVAKS, VADIM
<a href="#"><u>11132854</u></a>	Not Issued	30	05/19/2005	Asynchronous dual-queue interface for use in network acceleration architecture	MAKHERVAKS, VADIM
<a href="#"><u>11163958</u></a>	Not Issued	30	11/04/2005	METHOD OF OFFLOADING ISCSI PDU CORRUPTION-DETECTION DIGEST GENERATION FROM A HOST PROCESSING UNIT, AND RELATED ISCSI OFFLOAD ENGINE	MAKHERVAKS, VADIM
<a href="#"><u>11199765</u></a>	Not Issued	71	08/09/2005	Remote I/O for virtualized systems	MAKHERVAKS, VADIM
<a href="#"><u>11217196</u></a>	Not Issued	30	09/01/2005	Method of offloading iSCSI TCP/IP processing from a host processing unit, and related iSCSI TCP/IP offload engine	MAKHERVAKS, VADIM
<a href="#"><u>11301110</u></a>	Not Issued	61	12/12/2005	Memory operations in a virtualized system	MAKHERVAKS, VADIM
<a href="#"><u>11333028</u></a>	Not Issued	30	01/17/2006	Network protocol stack isolation	MAKHERVAKS, VADIM
<a href="#"><u>11839185</u></a>	Not Issued	20	08/15/2007	COMPLETION COALESCING BY TCP RECEIVER	MAKHERVAKS, VADIM

<a href="#">11843938</a>	Not Issued	25	08/23/2007	DATA TRANSFER ERROR CHECKING	MAKHERVAKS, VADIM
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**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = MESH

First Name = ALEXANDER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09218407</u>	6225924	150	12/22/1998	TRANSMISSION CODING METHOD AND DEVICE	MESH, ALEXANDER
<u>10752921</u>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	MESH, ALEXANDER
<u>11171781</u>	Not Issued	30	06/30/2005	Software debug support for cache flush with access to external data location(s) through debug port	MESH, ALEXANDER
<u>11282093</u>	Not Issued	71	11/17/2005	Fast path memory read request processing in a multi-level memory architecture	MESH, ALEXANDER
<u>11695681</u>	Not Issued	25	04/03/2007	Method Circuit and System for Data Flow Control	MESH, ALEXANDER
<u>60150895</u>	Not Issued	159	08/26/1999	SYSTEMS, METHODS AND COMPUTER PROGRAM PRODUCTS FOR A USAGE AWARDS PROGRAM AND RELATED POINTS BASED AUCTION	MESHKIN, ALEXANDER B
<u>09648063</u>	Not Issued	161	08/25/2000	Systems, methods and computer program products for a usage awards program and related points based auction	MESHKIN, ALEXANDER B.
<u>10681331</u>	Not Issued	30	10/09/2003	System, method and apparatus for an integrated marketing vehicle platform	MESHKIN, ALEXANDER B.
<u>10831345</u>	Not Issued	30	04/26/2004	System, method and apparatus for an online sports auction	MESHKIN, ALEXANDER B.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="MESH"/>	<input type="text" value="ALEXANDER"/>	<input type="button" value="Search"/>

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**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = SOSTHEIM

First Name = TAL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09393582</a>	<a href="#">6601195</a>	150	09/09/1999	SWITCH ADAPTER TESTING	SOSTHEIM, TAL
<a href="#">09591216</a>	<a href="#">6829647</a>	150	06/09/2000	SCALEABLE HARDWARE ARBITER	SOSTHEIM, TAL
<a href="#">09773227</a>	Not Issued	161	01/31/2001	Data communications interfaces	SOSTHEIM, TAL
<a href="#">09810940</a>	<a href="#">7050437</a>	150	03/16/2001	WIRE SPEED REASSEMBLY OF DATA FRAMES	SOSTHEIM, TAL
<a href="#">09810941</a>	<a href="#">6947430</a>	150	03/16/2001	NETWORK ADAPTER WITH EMBEDDED DEEP PACKET PROCESSING	SOSTHEIM, TAL
<a href="#">10619960</a>	Not Issued	71	07/15/2003	Controlling flow of data between data processing systems via a memory	SOSTHEIM, TAL
<a href="#">10619988</a>	<a href="#">7320041</a>	150	07/15/2003	CONTROLLING FLOW OF DATA BETWEEN DATA PROCESSING SYSTEMS VIA A MEMORY	SOSTHEIM, TAL
<a href="#">10619989</a>	Not Issued	41	07/15/2003	TRANSFERRING INTERRUPTS FROM A PERIPHERAL DEVICE TO A HOST COMPUTER SYSTEM	SOSTHEIM, TAL
<a href="#">10752921</a>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	SOSTHEIM, TAL
<a href="#">11217196</a>	Not Issued	30	09/01/2005	Method of offloading iSCSI TCP/IP processing from a host processing unit, and related iSCSI TCP/IP offload engine	SOSTHEIM, TAL
<a href="#">60192085</a>	Not Issued	159	03/24/2000	Asic with embedded processor for wire speed processing	SOSTHEIM, TAL

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 **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = YIFRACH

First Name = SHAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10752921</a>	Not Issued	30	01/07/2004	Dynamic management of TCP reassembly buffers	YIFRACH, SHAUL
<a href="#">11217196</a>	Not Issued	30	09/01/2005	Method of offloading iSCSI TCP/IP processing from a host processing unit, and related iSCSI TCP/IP offload engine	YIFRACH, SHAUL
<a href="#">11282093</a>	Not Issued	71	11/17/2005	Fast path memory read request processing in a multi-level memory architecture	YIFRACH, SHAUL
<a href="#">11695681</a>	Not Issued	25	04/03/2007	Method Circuit and System for Data Flow Control	YIFRACH, SHAUL
<a href="#">11946102</a>	Not Issued	17	11/28/2007	Device, System, and Method of Handling Delayed Transactions	YIFRACH, SHAUL
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